

## **Aspen Test Engineering Services for**



## In-System Programming

Experts in providing quality test solutions for over 20 Years

## **Aspen Test Engineering Delivers Quality ISP**

ISP (In-System-Programming) is programmed by loading information into a logic or memory device on a PCB and programming flash embedded in microcontrollers.

- ▲ Programmable devices for Flash, OTPs, NVRAMs, EEPROMs and FPGAs
- ▲ Programming on your Teradyne TS12X or GR228X tester.
- ▲ Specialty microcontrollers including: ST10, PIC Micro and Atmel AVR
- ▲ Solutions that are many times faster than off-the-shelf bootstrap loaders

See the List of available Programmable Devices on our website at: http://aspentest.com/ISP-Device-List.html

## Reasons to Trust Aspen Engineering with your ISP needs:

- ▲ Program board-unique information is securely encoded including: unique identification data, serial numbers or calibration values.
- ▲ In-circuit test solutions that reduce production costs, while eliminating a dedicated programming station and additional handling.
- ▲ Aspen Test Engineering's ISP solutions use the fastest programming algorithms available for the target device. We have accelerated techniques that can program multiple devices in parallel and skip through blocks of un-programmed locations.
- ▲ Quality programming in your microcontroller flash programmed for your business to comply with factory pin-test program and with the final user code.

Contact Aspen Test Engineering today to discuss your in system programming and solutions for TERADYNE®/GENRAD® systems

